Intel Docket No.: P16912

**Patent** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of: CLEMONS et al.	Examiner: Not yet assigned
Serial No.: Not yet assigned	) Group Art Unit: Not yet assigned
Filing Date: Herewith	INTEL Docket No.: P16912
For: INTEGRATED CIRCUIT PACKAGING DESIGN AND METHOD	) ) ) )

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants submit herewith patents, publications or other information of which they are aware that they believe may be material to the examination of this application, and in respect of which, there may be a duty to disclose.

The filing of this information disclosure statement shall not be construed as a representation that a thorough search has been made, an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists. Nor shall the filing of this information disclosure statement be construed as an admission against interest in any manner.

This Information Disclosure Statement is filed in accordance with 37 CFR §§1.56, 1.97 and 1.98. The items listed on the accompanying Form PTO-1449 may be deemed to be pertinent to the above-identified application and are made of record to assist the Patent and Trademark Office in its examination of this application. The Examiner is respectfully requested to fully consider the items listed on the enclosed copy of Form PTO-1449 and to independently ascertain their teaching.

No fee is believed to be due under 37 CFR §1.17(p) for this Information Disclosure Statement since it is being filed concurrently with the above-identified application.

Respectfully submitted,

December 4, 2003

Date

Nandu A. Talwalkar

Registration No. 41,339

Buckley, Maschoff & Talwalkar LLC

Five Elm Street

New Canaan, CT 06840

(203) 972-0049

Substitute for form 1449A/PTO		Complete if Known			
				Application Number	Not yet assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing Date	Herewith		
		First Named Inventor	CLEMONS		
				Group Art Unit	Not yet assigned
(use as many sheets as necessary)		Examiner Name	Not yet assigned		
Sheet	1	of	1	Intel Docket Number	P16912

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
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Examiner	Date
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